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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
NP	KAWAMURA, S. et al., "Cox-Rower Architecture for Fast Parallel Montgomery Multiplication", Eurocrypt 2000, LNCS 1807, pp.523-538, May 2000.
NP	Posch, K. et al., "Modulo Reduction in Residue Number Systems", IEEE Transactions on Parallel and Distributed Systems, Vol. 6., No. 5, pp.449-451, May 1995.
NP	Quisquater, J. J. et al., "Fast Decipherment Algorithm For RSA Public-Key Cryptosystem", Electronics Letters, Vol. 18, No. 21, pp. 905-907,(1982).
Examiner N. *Poul*	Date Considered 6/28/05

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